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### VERILOG Links

ASICs the website : This website is basically the "ASIC....book". ... These tools include HDL checking, coverage analysis, test suite analysis and FSM ...  
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### Switching From ASIC Design

This page provides an overview for ASIC designers moving to FPGA design. ... FPGA design iterations to test in-system immediately, FPGA design tools such as ...  
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### Untitled Document

For Leading ASIC Design Tools and IP Libraries. New Synplify ASIC Product Flow-Tested With Physical Design and Test Tools. SUNNYVALE, Calif., June 4, ...  
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### ASIC FAQ - RF and Wireless - Maxim/Dallas

For final package test, a specific quote is required. Design Tools & Modeling ... Maxim has a set of analog bipolar cells that are available for ASIC ...  
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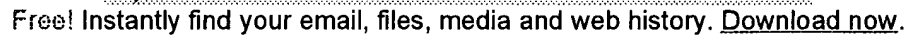
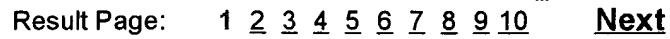
### EDN Access -- 09.29.94 ASIC Test: it's a new ball game

That's the good news about ASIC test. But there is a dirty little secret ... of tools involved in ASIC test is "test-development automation" (TDA) tools. ...  
[www.edn.com/archives/1994/092994/20dfcov.htm](http://www.edn.com/archives/1994/092994/20dfcov.htm) - 37k - [Cached](#) - [Similar pages](#)

### FPGA Prototyping Tool Aligns With ASIC Flow

... it easier for designers to test out a complex ASIC design on a single FPGA.

... Specifically, DC FPGA works well with the **ASIC** flow by accepting the ...  
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